S/N 09/476,219

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Robert J. Fite

Serial No.:

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Group Art Unit: 2838

Docket: 884.182US1

NOI

NON-LINEAR ADAPTIVE VOLTAGE POSITIONING FOR DC-DC

CONVERTERS

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Commissioner for Patents Washington, D.C. 20231

Applicant has reviewed the Office Action mailed on October 24, 2000. Please amend the above-identified patent application as follows.

IN THE DRAWINGS

The drawings were objected to under 37 CFR 1.83(a)and MPEP § 608.02(g). Corrected drawings are submitted herewith.

IN THE SPECIFICATION

On page 5, line 21, after "a high voltage level VI(k)", insert the text (", which corresponds to the minimum current voltage level. The minimum current voltage level VI(k) is the voltage at a nonzero minimum current level I(k) rather than a zero current level I(o), as is shown in Figure 3".

On page 8, line 1, please insert after "at 302 of Figure 3." the sentence "Similarly, a voltage signal with a nonlinear voltage response may be added to the output of a DC-DC converter or other voltage source and achieve a like result."

IN THE CLAIMS

Please amend the claims as follows:

1. (Amended) A method of providing a voltage from a DC-DC converter such that the voltage provided varies dependent on the current drawn from the DC-DC converter, comprising: sensing a current drawn from the DC-DC converter; and

adjusting the voltage provided from the DC-DC converter such that the voltage is at a ximum current voltage level when the current drawn is at a maximum load current level and

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